

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Prior Application: K. TANAKA et al  
Serial No. 10/303,841  
Filed: November 26, 2002

Group Art Unit: 2819  
Examiner: V. Tan  
For: LEVEL CONVERSION CIRCUIT AND  
SEMICONDUCTOR INTEGRATED CIRCUIT  
DEVICE EMPLOYING THE LEVEL CONVERSION  
CIRCUIT

PRELIMINARY AMENDMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

August 26, 2003

Sir:

Prior to examination, please amend the above application  
as follows.